

## Description

# [ELECTRO-MAGNETIC INTERFERENCE PROTECTION CIRCUIT AND METHOD FOR A CLOCK BUFFER]

### CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the priority benefit of Taiwan application serial no.92115368, filed on June 6, 2003.

### BACKGROUND OF INVENTION

[0002] Field of the Invention

[0003] This invention generally relates to an electro-magnetic interference protection circuit and a method, and more particularly to an electro-magnetic interference protection circuit and a method for a clock buffer.

[0004] Description of Related Art

[0005] As the technology advances, Internet becomes one of the most important resources for people to obtain knowledge and is a powerful tool for commercial transactions. Modem has been used in personal computers for Internet

connection. Today, high speed Internet solutions such as ADSL products are widely used for better quality. In those communication devices, the transceiver ICs are driven by clock signals that are distributed from clock buffers.

Hence, how to protect the clock buffers from electro-magnetic interference ("EMI") is an important issue.

[0006] Conventionally, a ferrite bead is serially connected to the power source of the clock buffer for EMI protection. However, because of its inductor characteristic, the ferrite bead also affects the waveform of the clock signal. FIG.1 is a timing diagram of the input clock signal clk-in and the output clock signal clk-out when the ferrite bead is serially connected to the power source of the clock buffer. As shown in FIG.1, the waveform of the output clock signal clk-out is affected by the ferrite bead. Hence, the communication devices cannot work properly because the highest voltage level and the lowest voltage level of the output clock signal clk-out are too close to be determined. Although the ferrite bead may protect the clock buffer from EMI, it affects the output clock signal of the clock buffer. Therefore, a solution for EMI protection without affecting the output clock signal of the clock buffer is needed.

## SUMMARY OF INVENTION

- [0007] An object of the present invention is to provide an electro-magnetic interference protection circuit and method for a clock buffer to protect the clock buffer from EMI and to keep the communication device work properly.
- [0008] In accordance with the above objects and other advantages of the present invention, an electro-magnetic interference protection circuit for a clock buffer is provided. The electro-magnetic interference protection circuit comprises a clock buffer for receiving a clock signal and buffer-outputting the clock signal; a ferrite bead, the ferrite bead serially being connected to the power source of the clock buffer to protect the clock buffer from electro-magnetic interference (e.g., the power source); and a capacitor connected in parallel with the ferrite bead to reduce the distortion of the clock signal due to the ferrite bead.
- [0009] In an embodiment of the present invention, the ferrite bead's impedance is at a maximum at the frequency of the electro-magnetic interference. For example, when the frequency of the electro-magnetic interference is 125MHz, the ferrite bead's impedance is at a maximum at 125MHz to protect the clock buffer from EMI.

[0010] In an embodiment of the present invention, the impedance of the capacitor is at a minimum at the frequency of the clock signal. For example, when the frequency of the clock signal is 25MHz, the impedance of the capacitor is at a minimum at 25MHz to reduce the distortion of a clock signal outputting by the clock buffer due to the ferrite bead.

[0011] The present invention provides an electro-magnetic interference protection method for a clock buffer. The method comprises the steps of: connecting a ferrite bead in serial with the power source of the clock buffer to protect the clock buffer from electro-magnetic interference (e.g., the power source); and connecting a capacitor in parallel with the ferrite bead to reduce the distortion of a clock signal outputting by the clock buffer due to the ferrite bead.

[0012] In an embodiment of the present invention, the ferrite bead's impedance is at a maximum at the frequency of the electro-magnetic interference. For example, when the frequency of the electro-magnetic interference is 125MHz, the ferrite bead's impedance is at a maximum at 125MHz to protect the clock buffer from EMI.

[0013] In an embodiment of the present invention, the impedance of the capacitor is at a minimum at the fre-

quency of the clock signal. For example, when the frequency of the clock signal is 25MHz, the impedance of the capacitor is at a minimum at 25MHz to reduce the distortion of a clock signal outputting by the clock buffer due to the ferrite bead.

[0014] Accordingly, the present invention not only uses a ferrite bead to protect the clock buffer from electro-magnetic interference, but also connects a capacitor in parallel with the ferrite bead to reduce the distortion of a clock signal output by the clock buffer due to the ferrite bead, thereby making the communication devices work properly.

[0015] The above is a brief description of some deficiencies in the prior art and advantages of the present invention. Other features, advantages and embodiments of the invention will be apparent to those skilled in the art from the following description, accompanying drawings and appended claims.

#### **BRIEF DESCRIPTION OF DRAWINGS**

[0016] FIG.1 is a timing diagram of the input clock signal clk-in and the output clock signal clk-out when the ferrite bead is connected in serial with the power source of the clock buffer.

[0017] FIG.2. is an embodiment of an electro-magnetic interfer-

ence protection method for a clock buffer in accordance with the present invention.

[0018] FIG.3 is the impedance characteristic curves in accordance with an embodiment of the present invention.

#### **DETAILED DESCRIPTION**

[0019] FIG.2. is an embodiment of an electro-magnetic interference protection circuit for a clock buffer in accordance with the present invention. The electro-magnetic interference protection circuit 200 includes a clock buffer 210, a ferrite bead 220, and a capacitor 230.

[0020] The clock buffer 210 receives the clock signal clk-in and buffer-outputs the clock signal clk-out to drive the transceiver IC (not shown in the figures) in the communication device. Generally, the power source is the main source of the EMI. For example, in this embodiment, an EMI source has been found by experiment in the power source of the clock buffer 210.

[0021] A ferrite bead 220 is connected to the power source terminal Vcc to protect the clock buffer from EMI at 125MHz. To achieve the best result, the ferrite bead 20 having a maximum impedance at 125MHz will be used. Referring to FIG.3, the curve A is the impedance characteristic curve for the ferrite bead 220 in this embodiment (Model No.

MLB-201209-450B-N3). Because curve A has a maximum impedance at 125MHz, the ferrite bead 220 corresponding to curve A can achieve the best result to protect the clock buffer 210 from EMI at 125MHz.

[0022] Furthermore, to prevent the clock signal clk-out from distortion by the ferrite bead 220, the capacitor 230 is connected in parallel with the ferrite bead 220. To achieve the best result, the capacitor 230 having a minimum impedance at the frequency of the clock signal clk-out will be used. In this embodiment, the frequency of clk-out is 25MHz. Hence, a 4.7 $\mu$ F capacitor is used because its impedance is at a minimum at 25MHz as showing in curve B of FIG.3. The resultant impedance characteristic curve of the ferrite bead 220 and the capacitor 230 is shown in curve C of FIG.3.

[0023] Accordingly, the present invention at least has the following advantages.

[0024] First, the clock buffer is protected from EMI.

[0025] Secondly, the communication device using the clock buffer can work properly.

[0026] Thirdly, the cost for protection from EMI is less because the present invention prevents the main EMI source (the power source) from interfering the clock buffer.

[0027] The above description provides a full and complete description of the preferred embodiments of the present invention. Various modifications, alternate construction, and equivalent may be made by those skilled in the art without changing the scope or spirit of the invention. Accordingly, the above description and illustrations should not be construed as limiting the scope of the invention which is defined by the following claims.